



Attorney's Docket No.: 10559-607001
Intel Ref.: P12891

JK
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : William R. Wheeler, et al.
Serial No. : 10/054,179
Filed : January 17, 2002
Title : MODELING A LOGIC DESIGN

Art Unit : 2825
Examiner : Annette M. Thompson
Assignee : Intel Corporation

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Entered
w/ RCE
dated
7-25-05

C. Storley
7-28-05

AMENDMENT IN REPLY TO FINAL OFFICE ACTION OF APRIL 22, 2005

Please amend the above-identified application as follows:

OK to Enter
S 10/31/05

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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